

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appln. No: To Be Assigned
Applicant: H. Hirano et al.
Filed: Herewith
Title: VOLTAGE DETECTION CIRCUIT, POWER-ON/OFF RESET CIRCUIT, AND SEMICONDUCTOR DEVICE
TC/A.U.:
Examiner:

DIVISIONAL OF:

Appln. No: 10/370,395
Applicant: Hiroshige Hirano et al.
Filed: February 20, 2003
Title: VOLTAGE DETECTION CIRCUIT, POWER-ON/OFF RESET CIRCUIT, AND SEMICONDUCTOR DEVICE
TC/A.U.: 2816
Examiner: Anh Quan Tra
Confirmation No.: 9820
Docket No.: MTS-2101US2

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the above-identified application as follows:

- ☒ **Amendments to the Specification** begin on page 2 of this paper.
- ☒ **Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.
- ☐ **Amendments to the Drawings** begin on page _____ of this paper and include an attached replacement sheet(s).
- ☐ **Amendments to the Abstract** are on page _____ of this paper. A clean version of the Abstract is on page _____ of this paper.
- ☐ **Remarks/Arguments** begin on page _____ of this paper.